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PTO/SB/21 (08-00)

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/025,509
	Filing Date	December 19, 2001
	First Named Inventor	Richard E. Kessler, et al.
	Group Art Unit	2635
	Examiner Name	Not yet assigned
Total Number of Pages in This Submission	Attorney Docket Number	005655.P004

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1 of 2

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U.S. PATENT DOCUMENTS

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Sheet 2 of 2

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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		JUN, B., et al., "The INTEL Random Number Generator," Cryptography Research, Inc. White Paper prepared for Intel, 8 pages, April 22, 1999	
		SCHNEIER, B., Applied Cryptography, Second Edition, published by John Wiley & Sons, Inc., chapter 8 Key Management and chapter 19 Public-Key Algorithms, 1996	
		YEE, B., "Using Secure Coprocessors," School of Computer Science, Carnegie Mellon University, 94 pages May 1994	
		Data Sheet 6500 Public Key Processor, Hi/fn, Inc. 45 pages, 1997-2000	
		Reference Hardware 6500 Public Key Processor, Hi/fn, Inc. 9 pages, 1997-1999	
		6500 Data Windowing Examples Application Note, Hi/fn, Inc. 9 pages, 1997-1999	

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